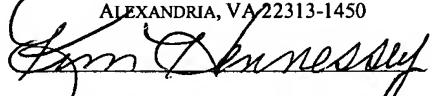




4-205
JFN
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: §
Olof Tornblad et al. § Group Art Unit: 2811
§
Serial No.: 10/678,837 § Examiner: Soward, Ida M.
§
Filing Date: October 3, 2003 §
§
Title: LDMOS Transistor § Atty. Docket No. 068736.0231
§ Client Ref.: 2003P52734US

Mail Stop Response
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING VIA EXPRESS MAIL	
PURSUANT TO 37 C.F.R. § 1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE WILL BE DEPOSITED WITH THE U.S. POSTAL SERVICE AS EXPRESS MAIL POST OFFICE TO ADDRESSEE, ON THE DATE BELOW, AND IS ADDRESSED TO:	
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Sir:

RESPONSE TO OFFICE ACTION DATED February 10, 2005

In response to the Office Action mailed February 10, 2005, Applicants respectfully requests reconsideration of the rejections set forth in the Office Action in light of the remarks below.

As the three-month shortened statutory period for reply is due May 10, 2005, this Response is therefore considered to be timely filed.